## EE 508 Lecture 37

**Digital Filters** 

### **Dynamic Range**



Numerous definitions for DR include some "qualitative" terms in the definition making it difficult to identify a universally accepted definition of the DR though the concept is useful

SNDR is a metric that is rigorously defined that captures some of the DR properties

Though the concept of DR is often not discussed rigorously and though there are various definitions of DR, Dynamic Range should be the primary driver of signal swing, power dissipation, and architecture selection not only in filter circuits but in analog circuit design in general

**Statistical Characterization of Noise** 



### Parseval's Theorem

$$\sqrt{\int_{t=0}^{\infty} \mathrm{Sdf}} = \lim_{T \to \infty} \int_{t_1}^{t_1+T} \mathcal{V}^2(t) dt$$

### Analysis of Noise in Filter Circuits

Consider a filter circuit with N noise voltage sources (can be easily modified to include both noise voltage and current sources)



The noise sources can be represented by the block diagram shown below



Assume  $T_k(s)$  is the transfer function from the kth source to the output

By superposition

$$V_{OUT}(s) = \sum_{i=1}^{N} T_i(s) V_i(s)$$

# Input-Referred Noise in Filter Circuits



$$\mathcal{V}_{OUT_RMS} = \sqrt{\int_{f=0}^{\infty} S_{OUT} df} = \sqrt{\int_{f=0}^{\infty} \sum_{i=1}^{N} S_i \bullet \left| T_i \left( j\omega \right) \right|^2 dt}$$

Let T(s) be the transfer function from the input to the output. (usually T(s) will be distinct from each of the noise transfer functions).

The input-referred noise spectral density is given by the expression

$$S_{IN} = \frac{S_{OUT}}{\left| T \left( j \omega \right)^2 \right|}$$

The input-referred RMS voltage is thus given by

$$\mathcal{U}_{IN\_RMS} = \sqrt{\int_{f=0}^{\infty} \frac{S_{OUT}}{\left|T(j\omega)\right|^2} df} = \sqrt{\int_{f=0}^{\infty} \sum_{i=1}^{N} S_i \cdot \frac{\left|T_i(j\omega)\right|^2}{\left|T(j\omega)\right|^2} df}$$

Example: First-Order RC Network



From a standard change of variable with a trig identity, it follows that

$$\mathcal{V}_{n_{RMS}} = \sqrt{\int_{f=0}^{\infty} S_{v_{OUT}} df} = \sqrt{\frac{kT}{C}}$$

- Note the continuous-time noise voltage has an RMS value that is independent of R
- The noise contributed by the resistor is dependent only upon the capacitor value C
- This is often referred to at kT/C noise and it can be decreased at a given T only by increasing C

**Theorem** If v(t) is a continuous-time zero-mean noise source and  $\langle v(kT) \rangle$  is a sampled version of v(t) sampled at times T, 2T, .... then the RMS value of the continuous-time waveform is the same as that of the sampled version of the waveform. This can be expressed as  $v_{\rm \tiny RMS} = \hat{v}_{\rm \tiny RMS}$ 

**Theorem** If v(t) is a continuous-time zero-mean noise signal and < v(kT) > is a sampled version of v(t) sampled at times T, 2T, .... then the standard deviation of the random variable v(kT), denoted as  $\sigma_v$ 

satisfies the expression  $\sigma_{\rm v}$  =  $\vartheta_{\rm RMS}$  =  $\vartheta_{\rm RMS}$ 

Example: Switched Capacitor Sampler



kT C V n<sub>RMS</sub>

What is the RMS value of the output noise voltage due to the noise on  $R_s$ ?



What is the RMS value of the output noise voltage due to the noise on  $R_L$  and  $R_S$ ?



## **Digital Filters**

### **Limitations of Analog Filters**

- Transfer functions sensitive to component and process variations
- Distortion inherent due to nonlinearities in components (particularly amplifiers)
- Power dissipation can be large
- Area gets large, often unacceptably so for very low frequency poles and even of concern for audio-frequency poles
- Programmability introduces considerable complexity (with existing approaches)
- Making minor changes in filter requirements often necessitates a major redesign effort

### **Advantages of Analog Filters**

Significant advantages but since comparative, will discuss later

### Analog vs Digital Filter



## Analog vs Digital Filter



- Imaginary axis for analog filters corresponds to unit circle for digital filters
- Several standard mappings used to map between s-domain and z-domain
- Some actually map imaginary axis to unit circle



### **Filter Design Processes**





for 
$$\beta_0 = 1$$
 and  $a_i = \alpha_i$   
 $b_i = \beta_i$   $y(nT) = \sum_{i=0}^m a_i x(nT - iT) + \sum_{i=1}^n b_i y(nT - iT)$ 

If n=1 termed a Moving Average (MA) - only zeros present

If m=1 termed an Auto Regressive (AR) - only poles present If mn  $\neq$  1 termed Autoregressive Moving Average (ARMA) – poles and zeros present



MA filters have a finite impulse response so are often termed FIR filters

If m=1 or mn  $\neq$  1 the filter will have one or more poles (and possibly many zeros but not necessarily any)

AR and ARMA filters have infinite impulse response so are often termed IIR filters

#### **Moving Average Filters**



Impulse Response



#### **Moving Average Filters**



#### **Moving Average Filters**



It can be shown that

$$y(n) = x(n) \otimes h(n) = \sum_{k=0}^{m} h(k) x(n-k)$$

FIR filters are sometimes termed convolutional filters

#### **Moving Average Filters**



#### FIR Filters can be easily designed to have linear phase





Moving Average Filters

FIR Filters can be easily designed to have linear phase

Example: m=7

$$\mathsf{H}\left(\mathsf{e}^{j\omega}\right) = a_{0}e^{-\frac{7}{2}j\omega} \left[2\cos\left(\frac{7}{2}\omega\right)\right] + a_{1}e^{-\frac{7}{2}j\omega} \left[2\cos\left(\frac{5}{2}\omega\right)\right] + a_{2}e^{-\frac{7}{2}j\omega} \left[2\cos\left(\frac{3}{2}\omega\right)\right] + a_{3}e^{-\frac{7}{2}j\omega} \left[2\cos\left(\frac{1}{2}\omega\right)\right]$$

X<sub>IN</sub>(t)

**Digital Filter** 

H(z)

 $a_2$ 

m=7 ∮Y

DAC

Y(kT)

X<sub>OUT</sub>(t)

kT

ADC

X(kT)

Regrouping, we obtain

$$\mathsf{H}\left(\mathsf{e}^{j\omega}\right) = e^{-\frac{7}{2}j\omega} 2\left(a_0\cos\left(\frac{7}{2}\omega\right) + a_1\cos\left(\frac{5}{2}\omega\right) + a_2\cos\left(\frac{3}{2}\omega\right) + a_3\cos\left(\frac{1}{2}\omega\right)\right)$$

It thus follows that

$$\left| \mathsf{H}\left(\mathsf{e}^{j\omega}\right) \right| = 2\left(a_0 \cos\left(\frac{7}{2}\omega\right) + a_1 \cos\left(\frac{5}{2}\omega\right) + a_2 \cos\left(\frac{3}{2}\omega\right) + a_3 \cos\left(\frac{1}{2}\omega\right)\right)$$
$$\angle \mathsf{H}\left(\mathsf{e}^{j\omega}\right) = -\frac{7}{2}\omega$$

Thus H(z) is linear phase !

This property holds for any symmetric impulse response of a FIR filter of any order



It is easy to design linear phase digital filters

$$y(nT) = \sum_{i=0}^{m} a_i x(nT - iT)$$

Theorem: Any FIR filter is linear phase if the impulse response is symmetric or antisymmetric

Туре	impulse response	
I	symmetric	length is odd
II	symmetric	length is even
III	anti-symmetric	length is odd
IV	anti-symmetric	length is even

#### Table from Robert Novak book



Theorem: Any FIR filter is linear phase if the impulse response is symmetric or antisymmetric

![](_page_22_Figure_2.jpeg)

![](_page_23_Figure_1.jpeg)

$$y(nT) = \sum_{i=0}^{m} a_i x(nT - iT) + \sum_{i=1}^{n} b_i y(nT - iT)$$

### An Implementation of a Digital Filter

![](_page_23_Figure_4.jpeg)

![](_page_23_Picture_5.jpeg)

**Delay Element** 

![](_page_23_Figure_7.jpeg)

**Multiply Element** 

![](_page_24_Figure_1.jpeg)

### An Implementation of a FIR Digital Filter

![](_page_24_Figure_3.jpeg)

- Delay operations or delay filters are easily implemented with digital filters
- Delay for each delay element is one clock period

### An Implementation of a Digital Filter

![](_page_25_Figure_1.jpeg)

![](_page_25_Figure_2.jpeg)

### An Implementation of an Analog Filter

![](_page_25_Figure_4.jpeg)

![](_page_25_Figure_5.jpeg)

- Can be viewed as analogous implementations
- Neither particularly practical
- Many other architectures for both analog and digital filters
- Approximately double the number of integrators or delay elements needed

### An Implementation of a Digital Filter

$$H(z) = \frac{\sum_{i=0}^{m} \alpha_i z^{-i}}{\sum_{i=0}^{n} \beta_i z^{-i}} \qquad \text{for } \beta_0 = 1 \text{ and } \qquad \begin{array}{l} a_i = \alpha_i \\ b_i = \beta_i \end{array} \qquad y(nT) = \sum_{i=0}^{m} a_i x(nT - iT) + \sum_{i=1}^{n} b_i y(nT - iT) \\ b_i = \beta_i \end{array}$$

![](_page_26_Figure_2.jpeg)

### An Implementation of an Analog Filter

![](_page_26_Figure_4.jpeg)

Termed Direct Synthesis or Analog Computer Approach

### Alternate Implementations of an Digital Filter

![](_page_27_Figure_1.jpeg)

- Reduced number of delay elements by factor of 2
- Still not particularly practical
- Similar architectural change can be made for analog filter (next slide)

### Alternate Implementations of an Analog Filter

![](_page_28_Figure_1.jpeg)

- Reduced number of integrators by factor of 2
- Still not particularly practical
- Similar architectural change for digital filter (previous slide)

### Alternate Implementations of an FIR Digital Filter

![](_page_29_Figure_1.jpeg)

![](_page_29_Figure_2.jpeg)

### Alternate Implementations of IIR Digital Filter

![](_page_30_Figure_1.jpeg)

![](_page_30_Figure_2.jpeg)

![](_page_30_Figure_3.jpeg)

Excessive delay elements but not of as much concern as excessive Integrators

## **Does Digital Filter Overcome Limitations**

![](_page_31_Figure_1.jpeg)

- A Transfer functions sensitive to component and process variations
- D Transfer function part of H(z) not sensitive to process variations
  - Transfer function sensitive to coefficient quantization
  - ADC and DAC minimally sensitive to process variations but highly sensitive to missmatch
- A Distortion inherent due to nonlinearities in components (particularly amplifiers)
- D Transfer function part of H(z) not sensitive nonlinearity of components
  ADC and DAC sensitive to nonlinearity of components
- A Power dissipation can be large
- D Power dissipation can be large due to a large number of arithmetic operations during each clock cycle
  - ADC and DAC dissipate considerable energy for high resolution or high speed

## **Does Digital Filter Overcome Limitations**

![](_page_32_Figure_1.jpeg)

- A Area gets large, often unacceptably so for very low frequency poles and even of concern for audio-frequency poles
- D Area for DSP in Digital Filter can be large
  - ADC and DAC can become large if high resolution is required
  - No area penalty for low frequency operation of digital system
- A Programmability introduces considerable complexity (with existing approaches)
- D Programmability of filter characteristics is very efficient with digital filter approach
- A Making minor changes in filter requirements often necessitates a major redesign effort
- D Making minor or even major changes in filter requirements requires minimal effort with digital filter approach

![](_page_33_Picture_0.jpeg)

# Stay Safe and Stay Healthy !

# End of Lecture 37